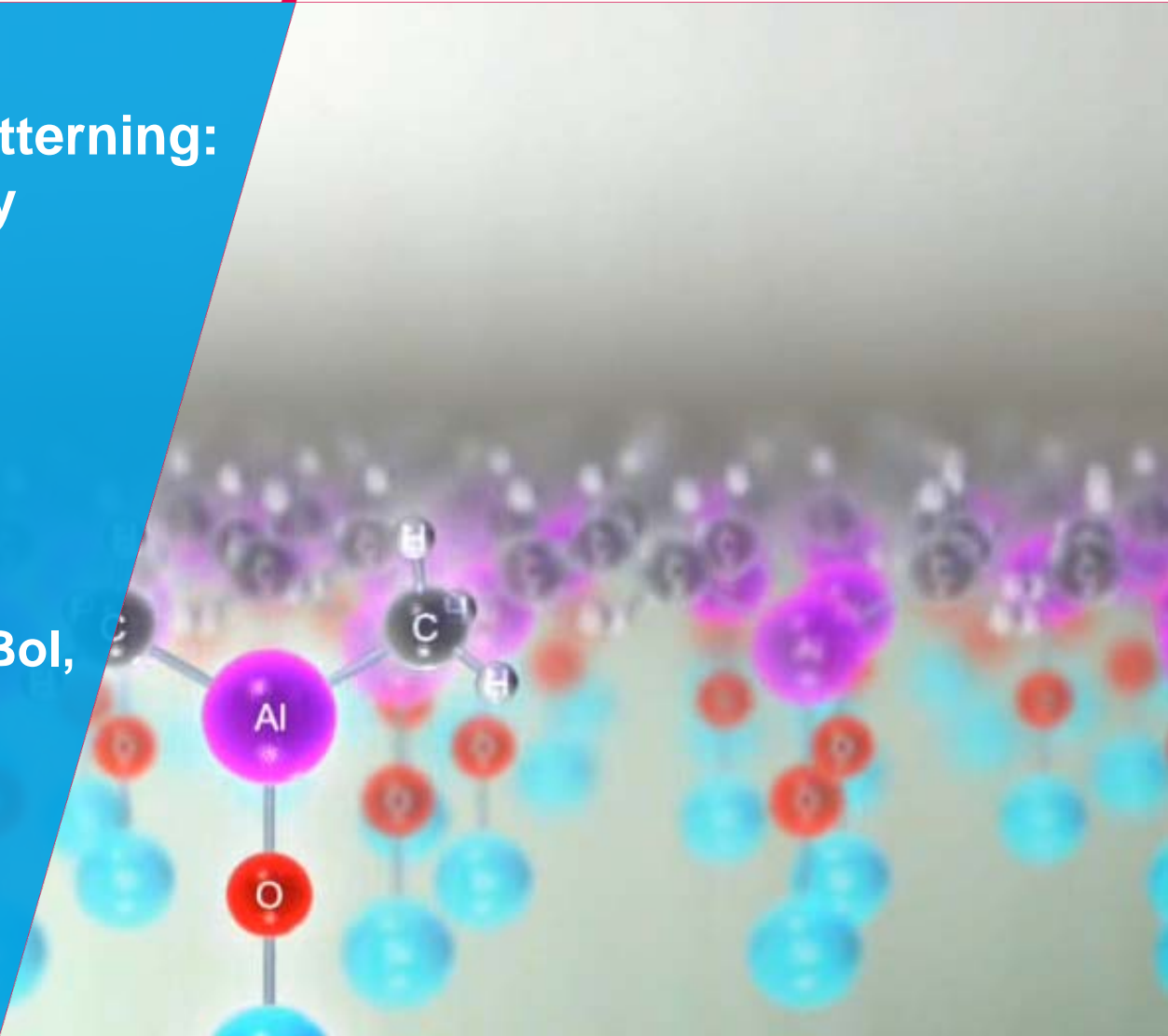


# ALD-enabled nanopatterning: area-selective ALD by area-activation

Adrie Mackus, Ageeth Bol,  
and Erwin Kessels

w.m.m.kessels@tue.nl  
www.tue.nl/pmp



# Outline

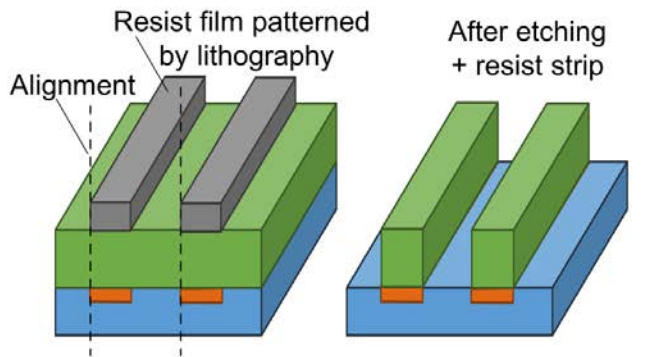
- Introduction & Area-selective deposition
- Area-selective ALD by **area-deactivation**
  - Example: **blocking with polyimide**
- Area-selective ALD by **area-activation**
  - Example: **EBID & ALD of Pt (direct-write ALD)**
  - Intermezzo:  $\mu$ -contact printing & ALD of Ru
  - Example:  **$\mu$ -plasma printing & ALD of  $\text{In}_2\text{O}_3$  (direct-write ALD)**
- Summary

# Selective deposition for bottom-up processing

## Top-down



Excavated from solid rock



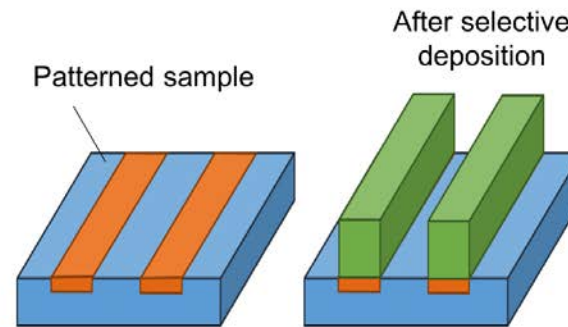
Current process flow  
Removal of excess material using etching

“Subtractive” processes

## Bottom-up



Bricks as building blocks



Future: **selective deposition**  
Only adding material where needed

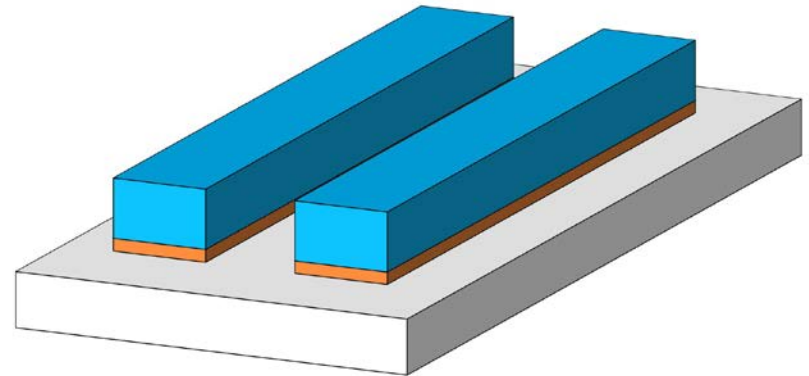
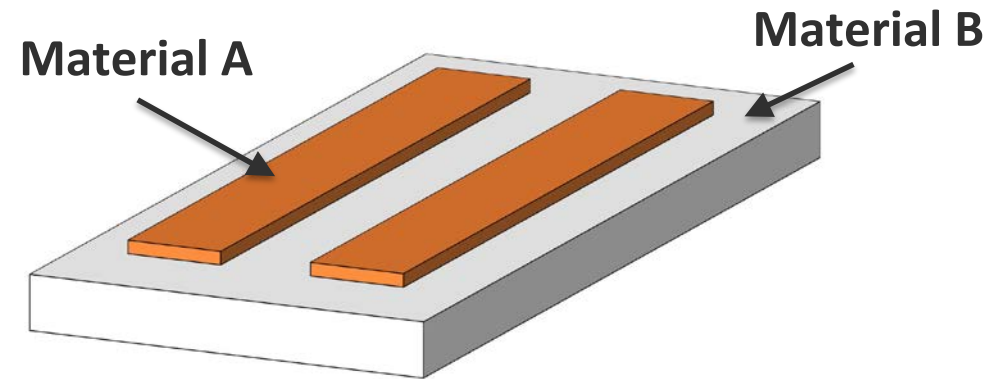
“Additive” processes

# Area-selective deposition

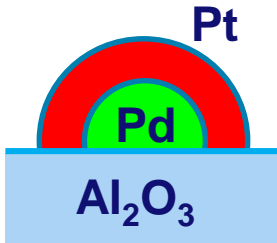
Surface with material A  
and material B



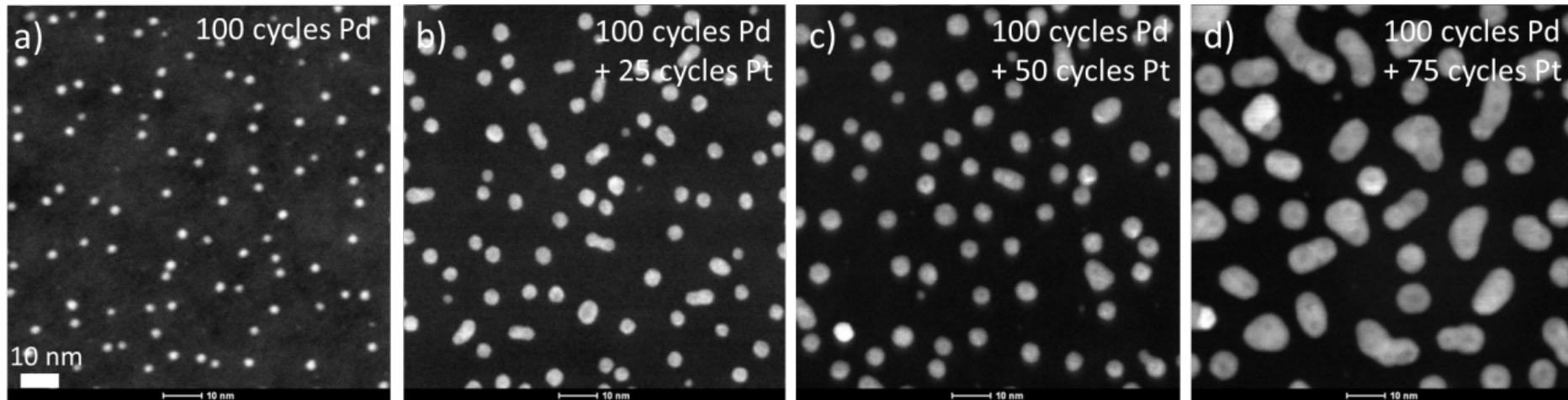
Growth on material A only,  
not on material B



# Area-selective deposition: core-shell nanoparticles



1. Deposit Pd nanoparticles by ALD
2. Deposit Pt shell by area-selective ALD on Pd core

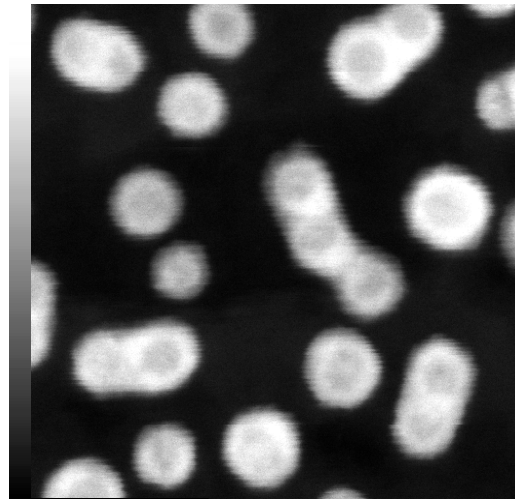
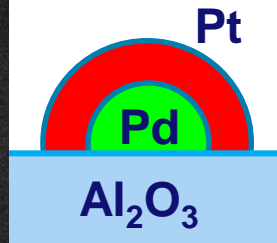
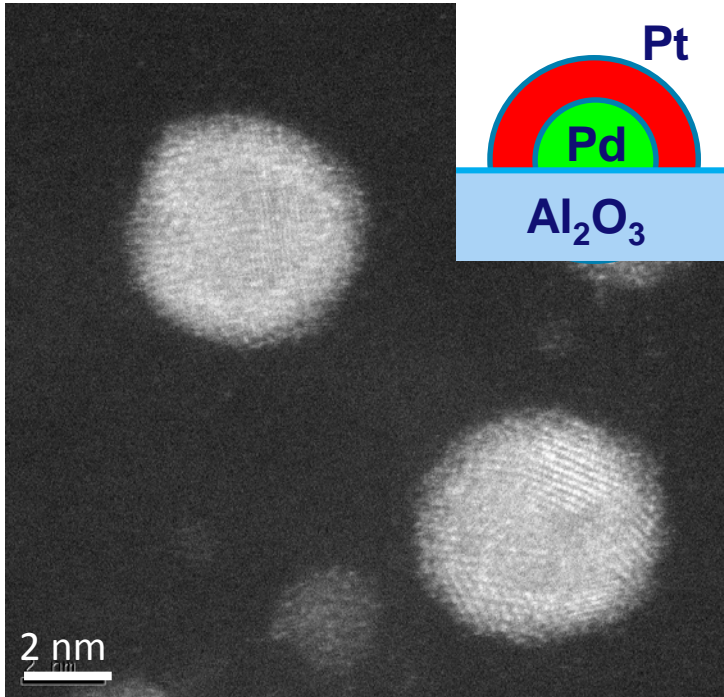


Fixed core diameter (100 cycles Pd)  
with **varying shell thickness** (0, 25, 50 and 75 cycles Pt)

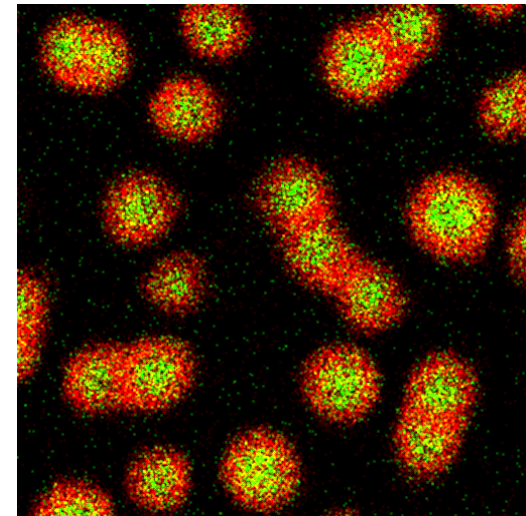


# Area-selective ALD: core-shell nanoparticles

HAADF-STEM imaging

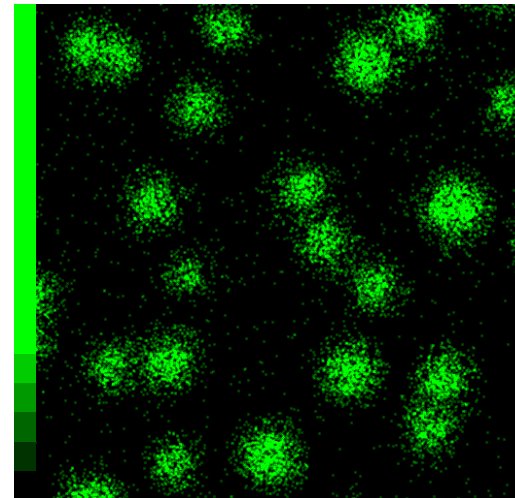


20 nm IMG1(frame1)

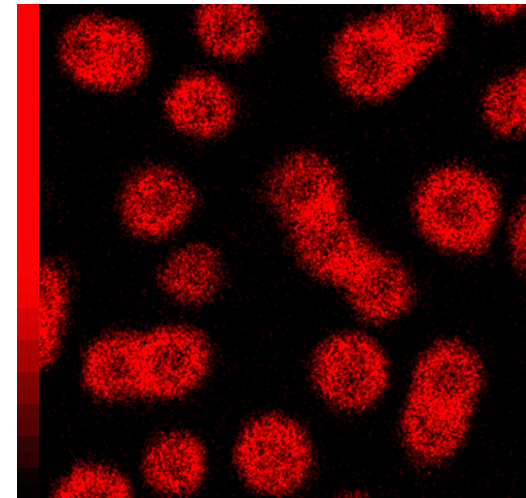


20 nm

EDS mapping



20 nm Pd L



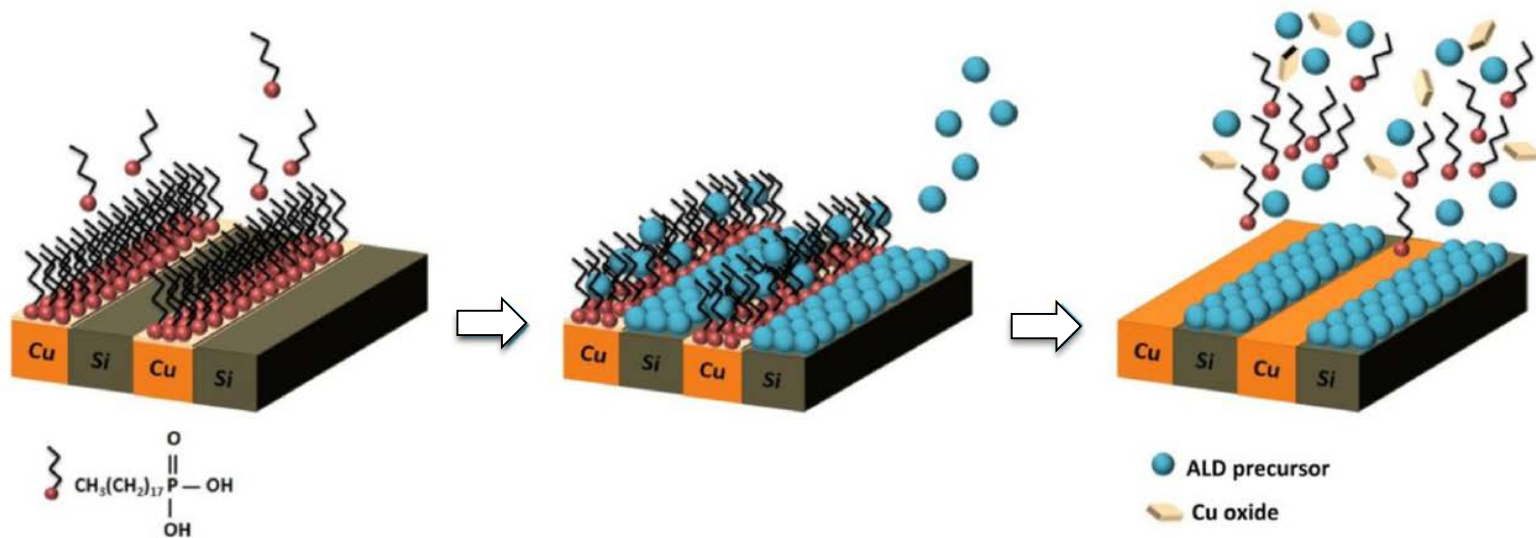
20 nm Pt M

# Area-selective deposition: $\text{Al}_2\text{O}_3$ on $\text{Cu}/\text{SiO}_2$

**Patterned  $\text{Cu}/\text{SiO}_2$ :**  
SAMS grow on  $\text{Cu}(\text{O}_x)$

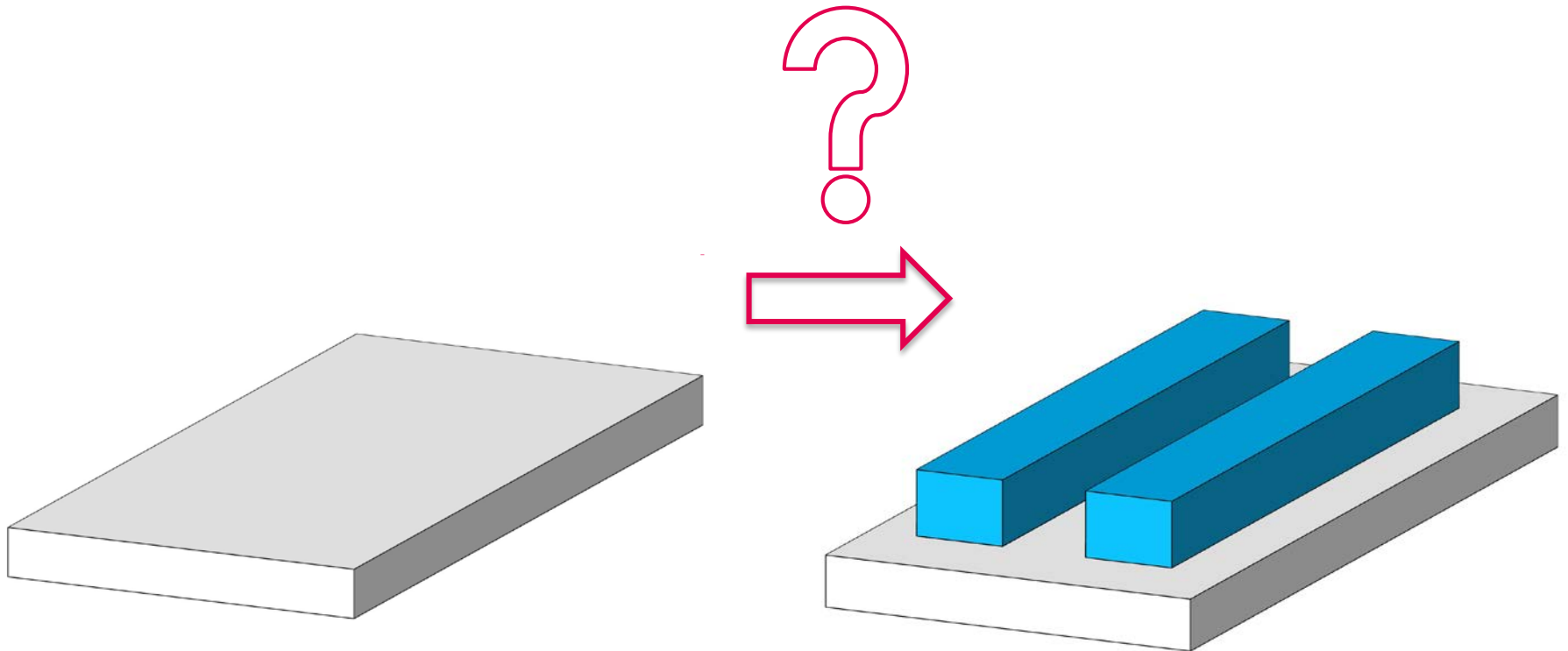
ALD of  $\text{Al}_2\text{O}_3$  on  $\text{SiO}_2$  and  
(ultimately) on SAMS

Selective etch of  $\text{Al}_2\text{O}_3$  and  
SAMS



- The SAMS grow **selectively** on the  $\text{Cu}(\text{O}_x)$  and not on the  $\text{SiO}_2$
- $\text{Al}_2\text{O}_3$  grows on **both the  $\text{SiO}_2$  and on the SAMS** (albeit with some delay and with a lower quality)

# Nanopatterning using area-selective ALD?

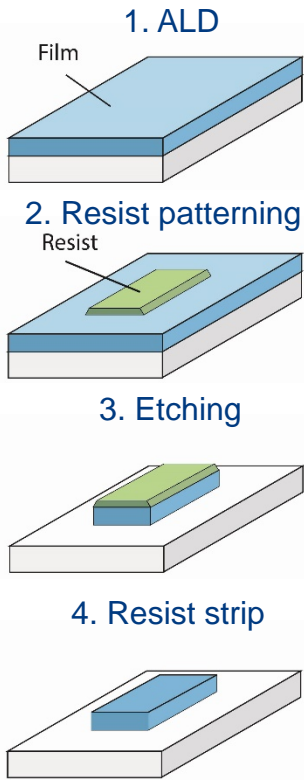




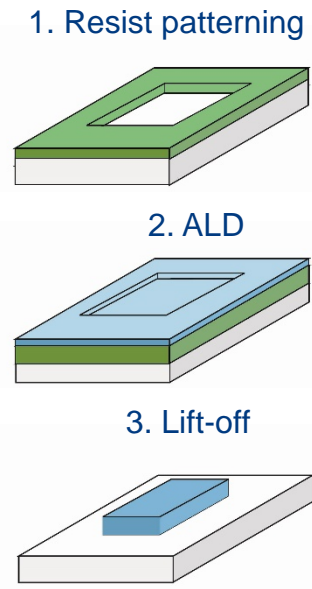
# ALD-enabled patterning

## Conventional:

### Etching

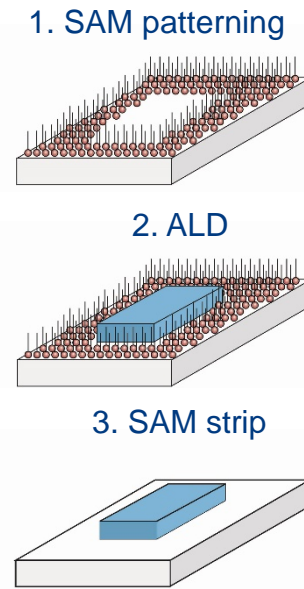


### Lift-off



## ALD-enabled:

### Area-selective ALD by area-deactivation



# Area-selective ALD by area-deactivation

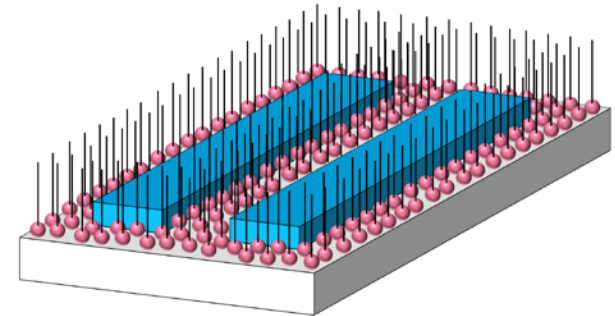
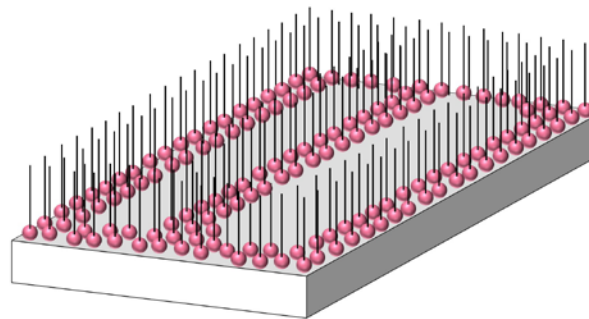
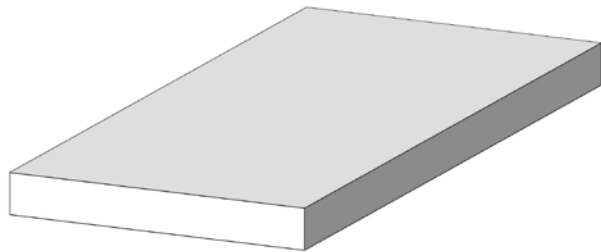
Blank substrate



Areas masked by  
photosensitive  
material or SAMs



ALD on open  
areas only

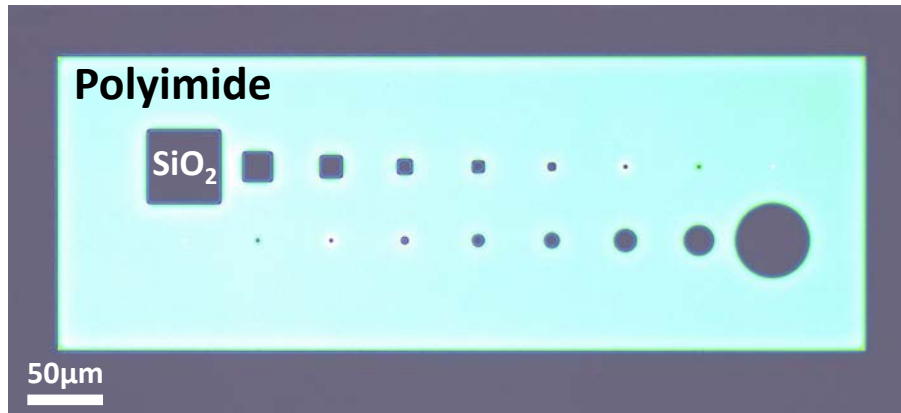


No deposition on the masking material (otherwise lift-off)

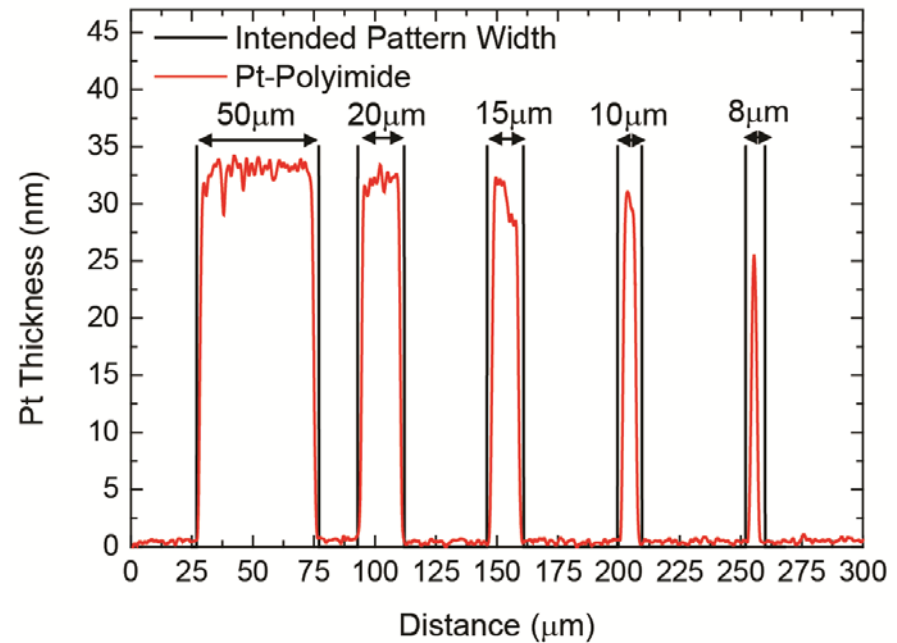
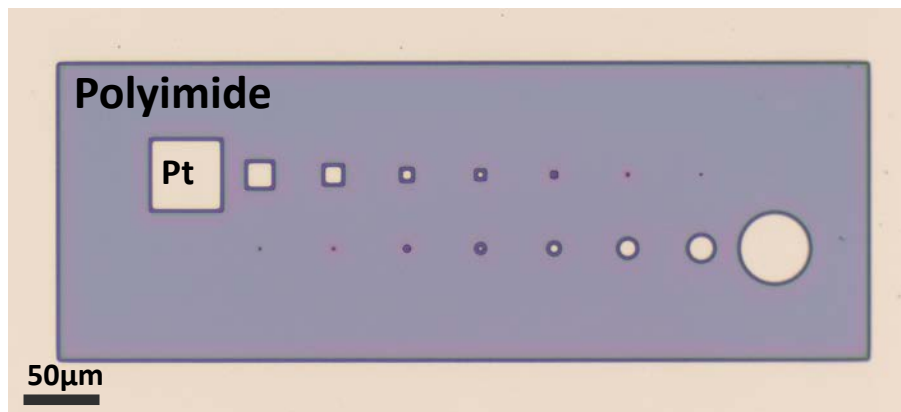
The masking of the substrate by photosensitive material or by SAMs requires a patterning step (typically subtractive)!

# AS-ALD by area-deactivation: using polyimide

After polyimide photolithography patterning

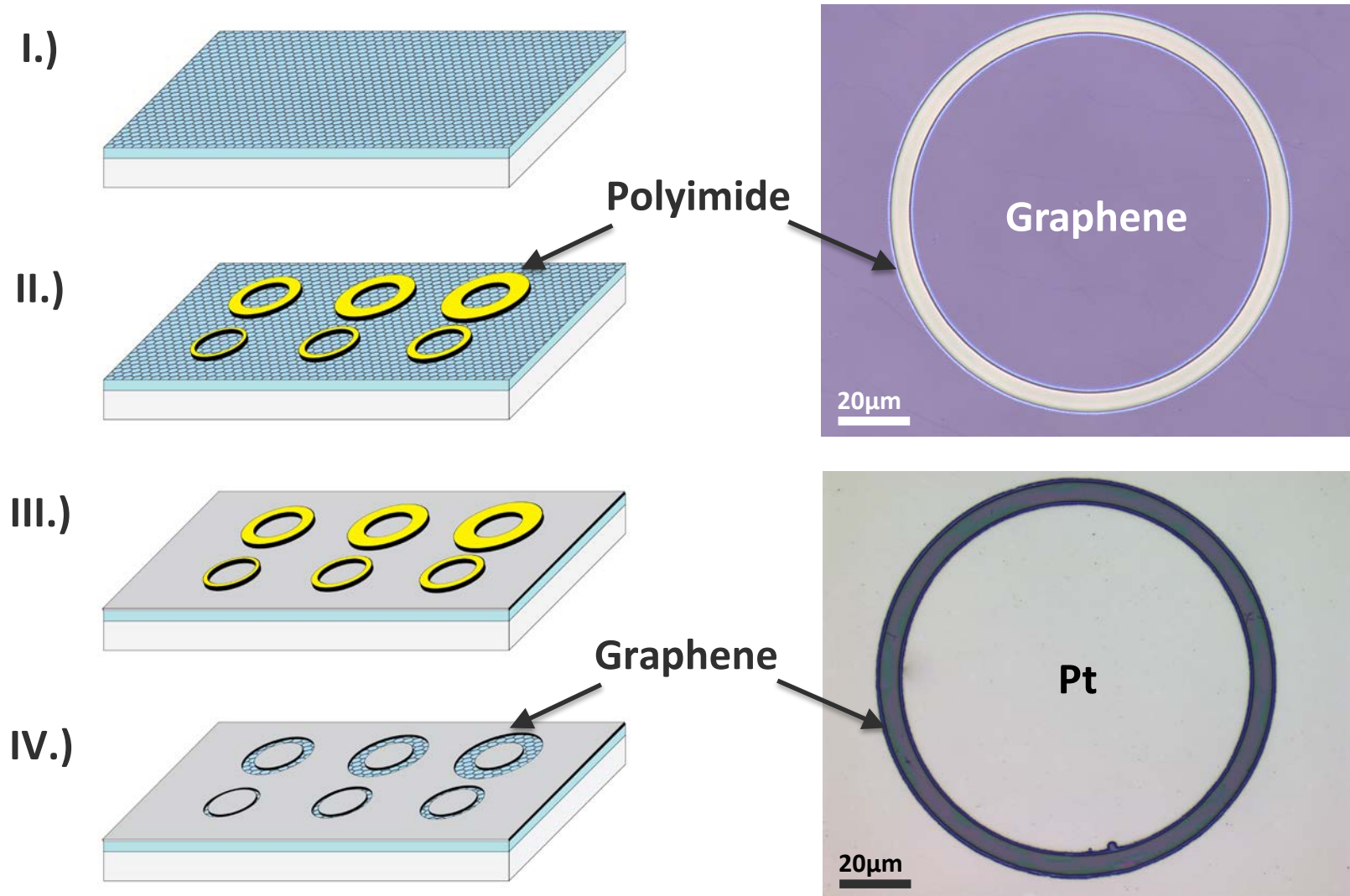


After 1000 cycles Pt ALD (300 °C)



- Pt does not deposit on polyimide so **no lift-off**
- Polyimide does not “flow” at 300 °C so **well-controlled pattern width**

# AS-ALD by area-deactivation: using polyimide



# Area-selective ALD by area-deactivation

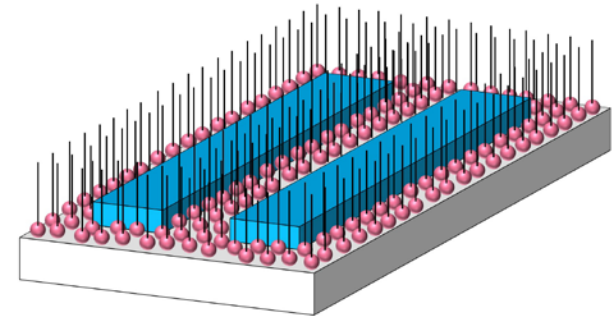
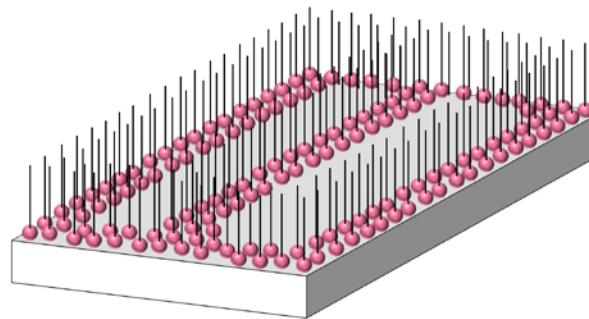
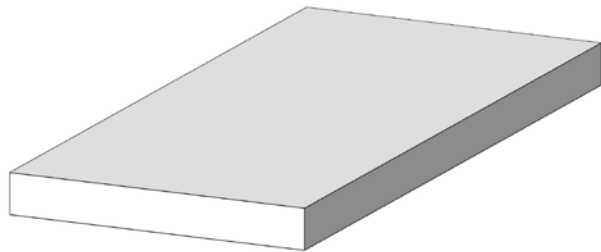
Blank substrate



Areas masked by  
photosensitive  
material or SAMs



ALD on open  
areas only



No deposition on the masking material (otherwise lift-off)

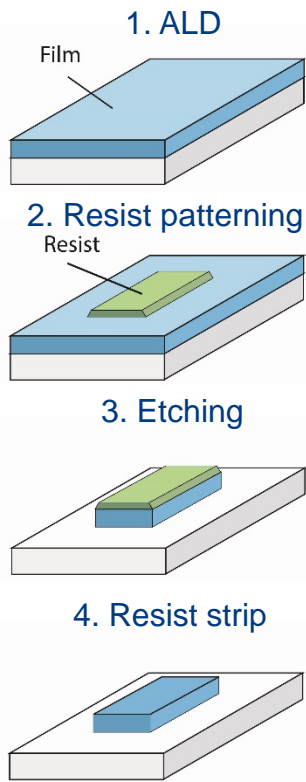
The masking of the substrate by photosensitive material or by SAMs requires a patterning step (typically subtractive)!



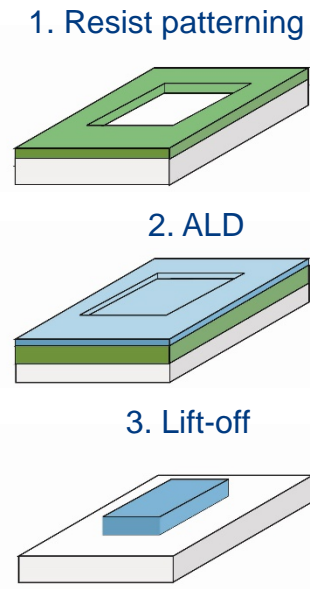
# Summary: ALD-enabled patterning

## Conventional:

### Etching

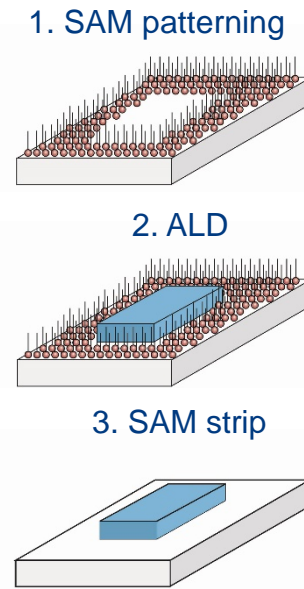


### Lift-off

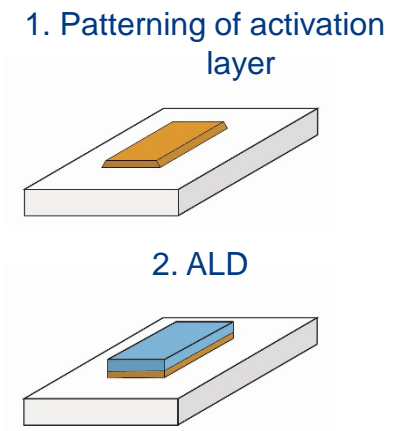


## ALD-enabled:

### Area-selective ALD by area-deactivation

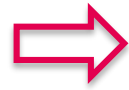


### Area-selective ALD by area-activation



# Area-selective ALD by area-activation

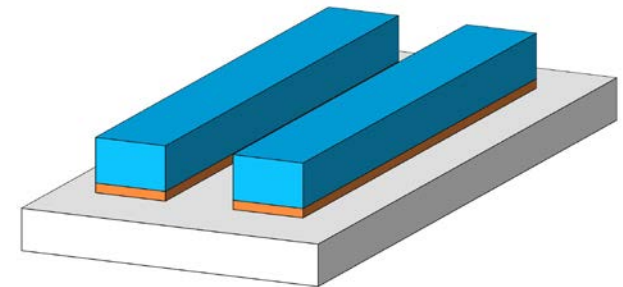
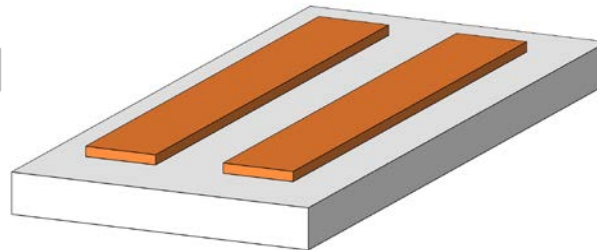
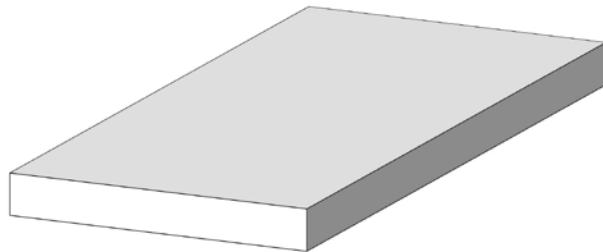
Blank substrate



Local activation of  
surface by seed layer  
or modification  
surface groups, etc.



ALD on activated  
areas only

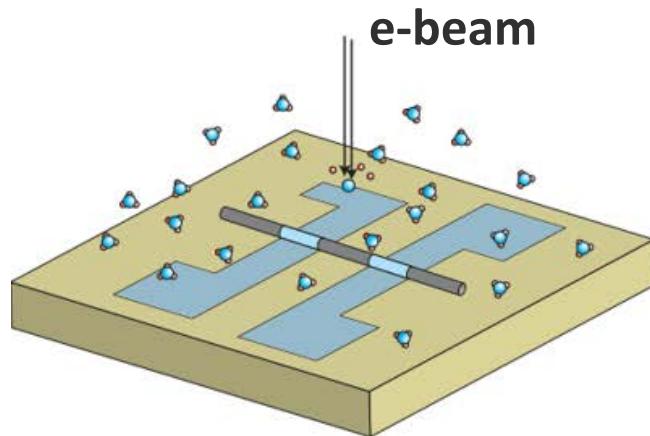


The local activation is a patterning step but  
it is not subtractive!

# AS-ALD by area-activation: EBID & ALD of Pt

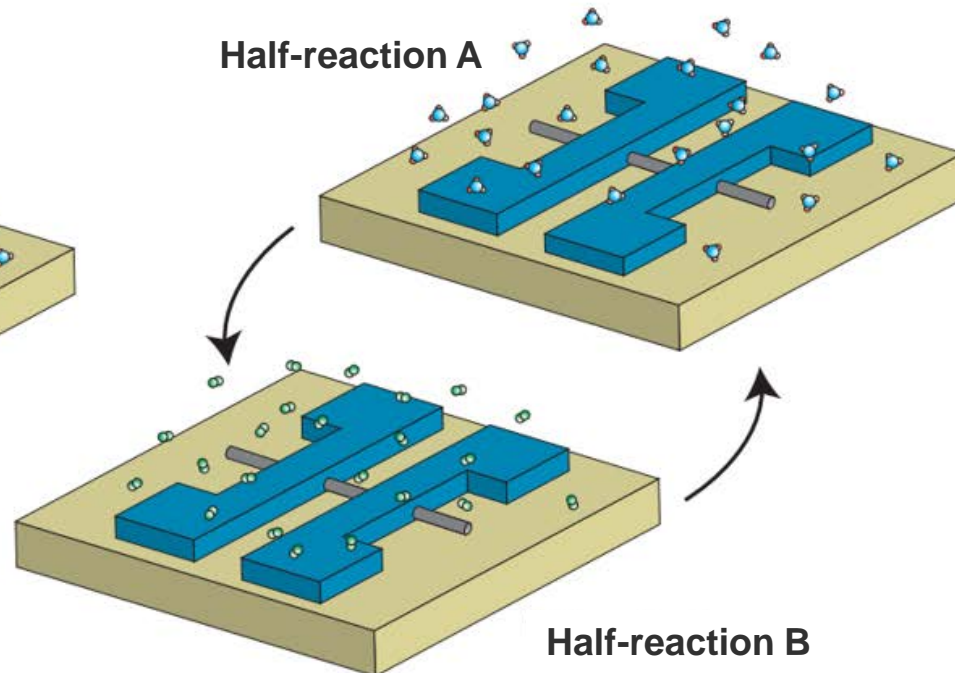
## 1. Patterning step:

e-beam induced deposition (EBID)



## 2. Building step:

Atomic layer deposition (ALD)



Two-step process:

- Patterning: ultrathin (<1 ML) seed layer on oxide by **EBID**
- Building: **area-selective ALD** on seed layer

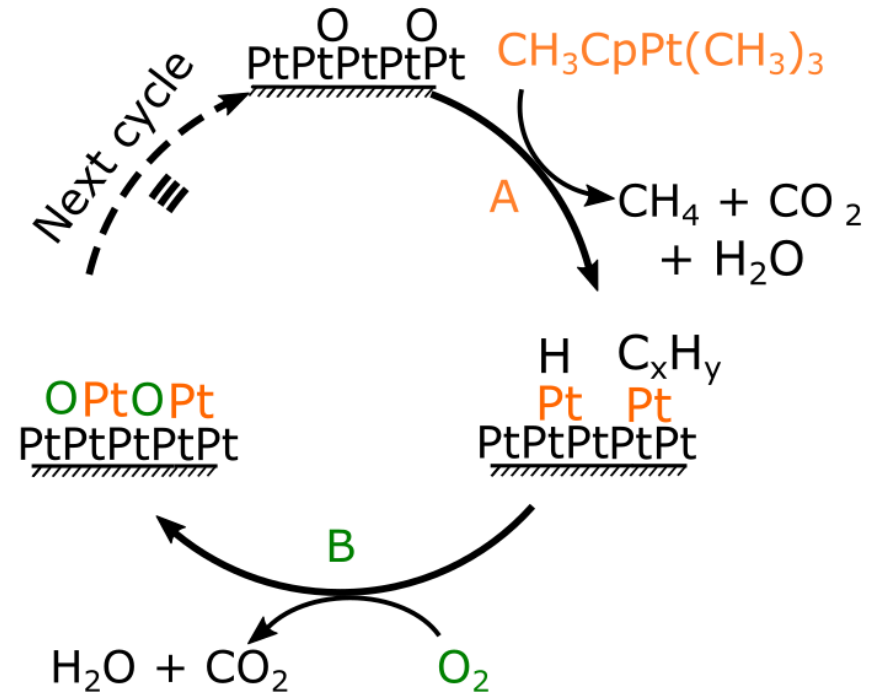
# AS-ALD by area-activation: EBID & ALD of Pt

## 1. Patterning step: EBID



FEI Nova Nanolab 600

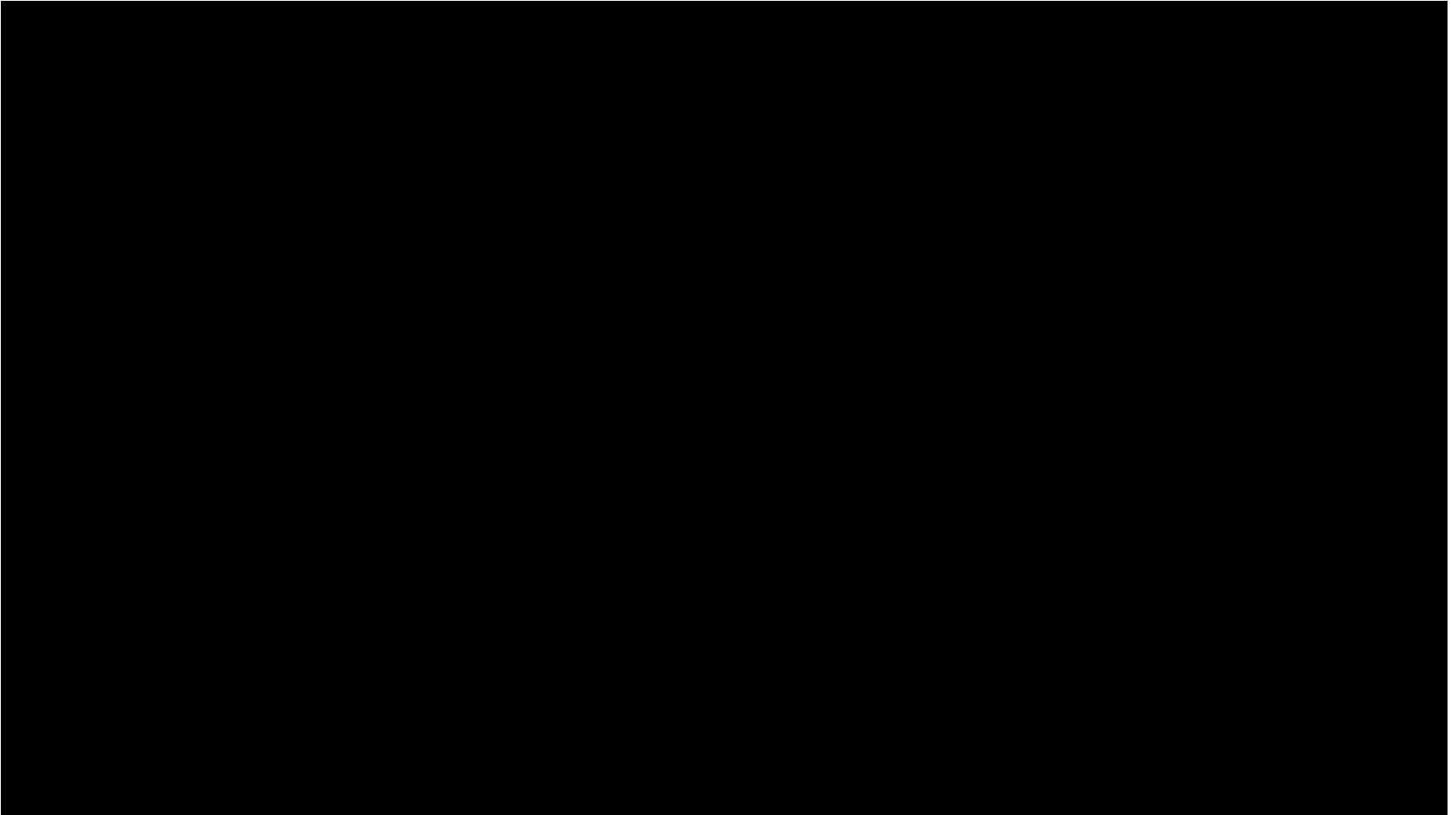
## 2. Building step: ALD



Patterning with:

- **Nanoscale resolution** of electron beam induced deposition (EBID)
- High Pt **material quality** as obtained by ALD

# Direct-write ALD with e-beam patterning

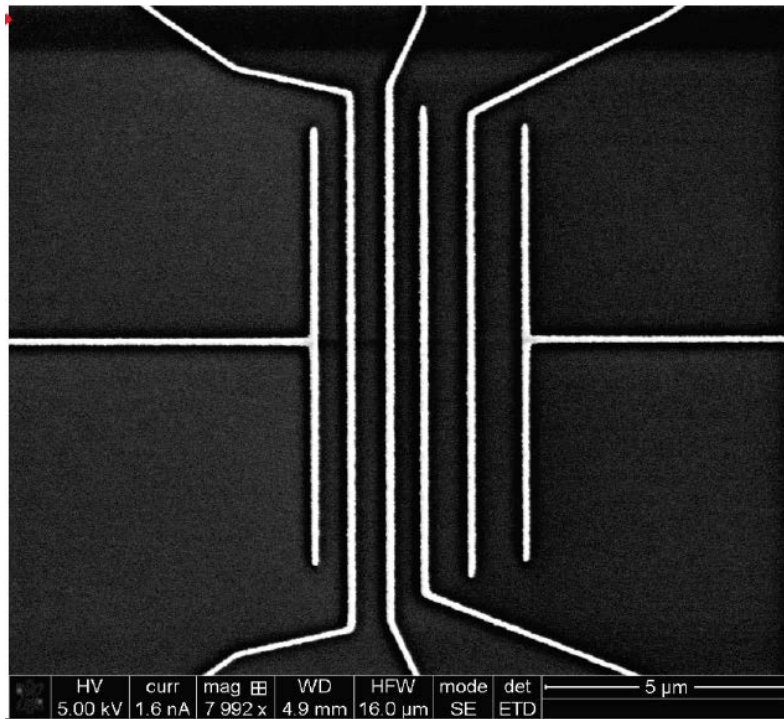


**Direct-write:** maskless and “templateless” patterning

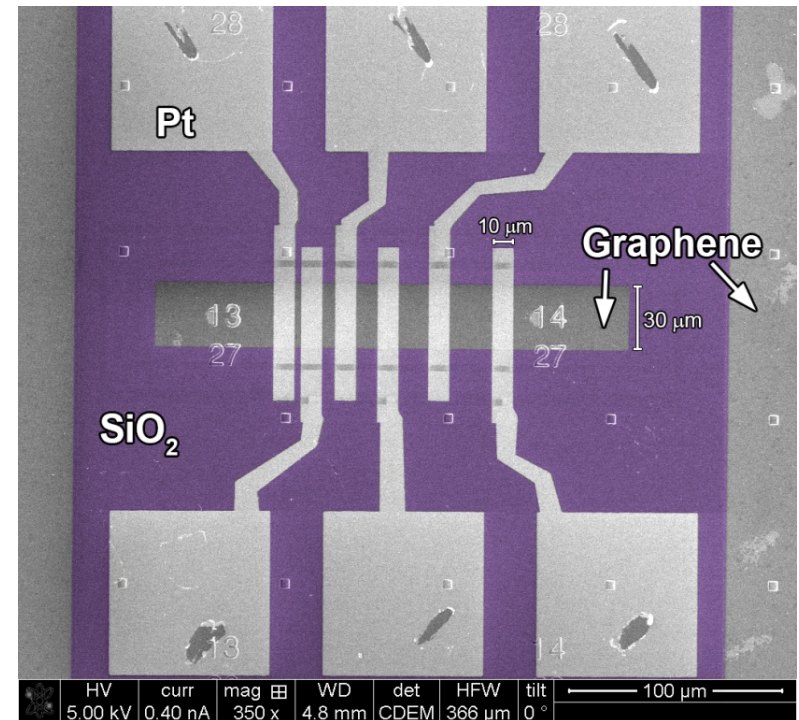


# Direct-write ALD of Pt contacts

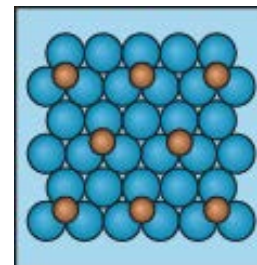
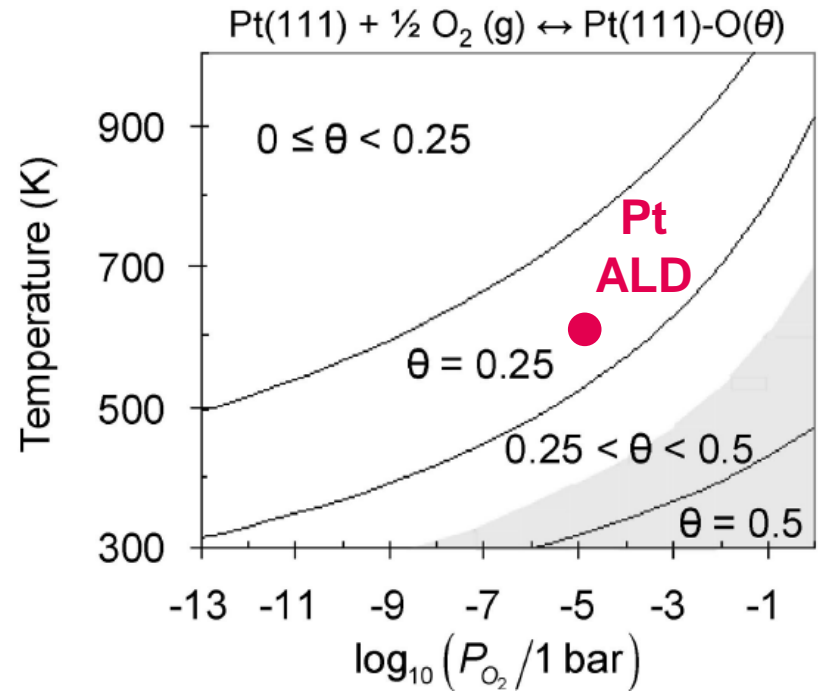
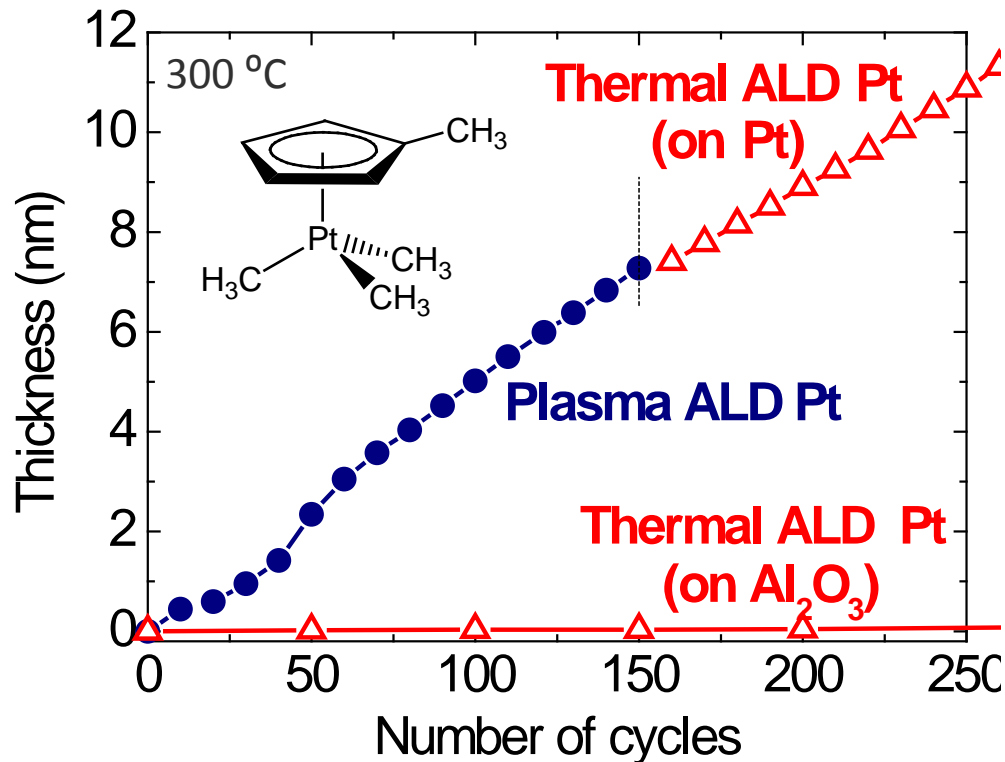
Back-gated (**single-wall**) **CNTFET**  
with direct-write ALD **Pt** contacts



TLM structure on **graphene**  
with direct-write ALD **Pt** contacts



# Underlying surface science: Pt dissociates O<sub>2</sub>



Pt nucleates **poorly** on oxides  
 Nucleation depends on **O<sub>2</sub> pressure** and **sample temperature**

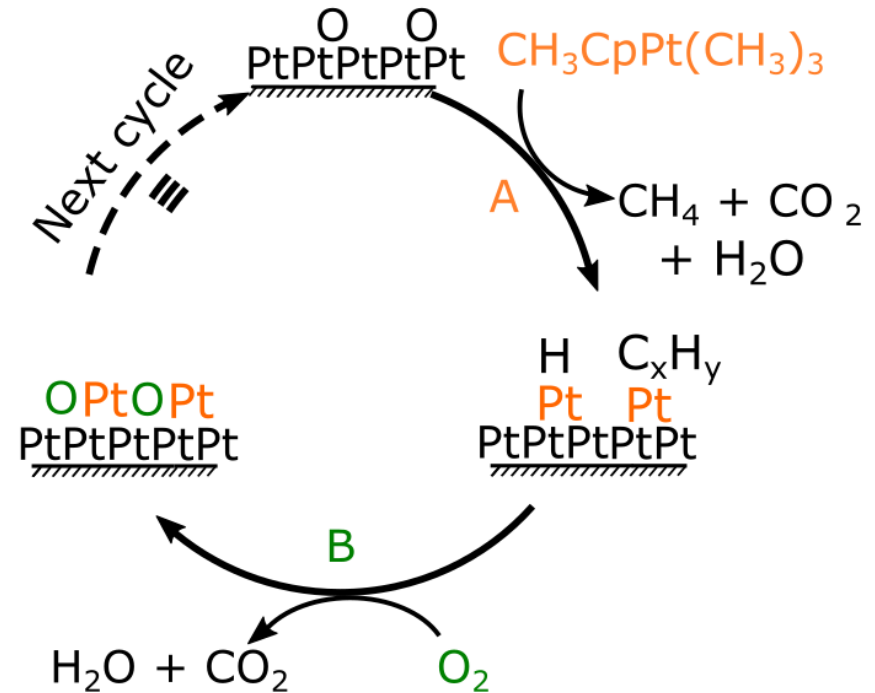
# AS-ALD by area-activation: EBID & ALD of Pt

## 1. Patterning step: EBID



FEI Nova Nanolab 600

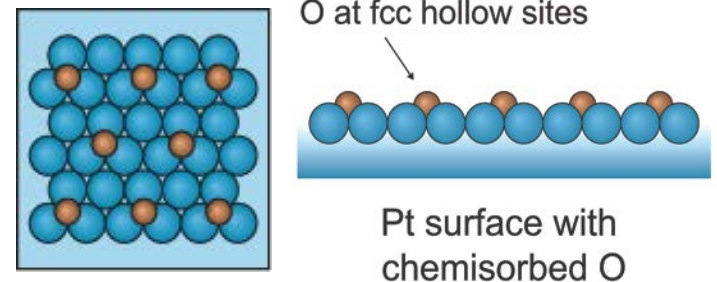
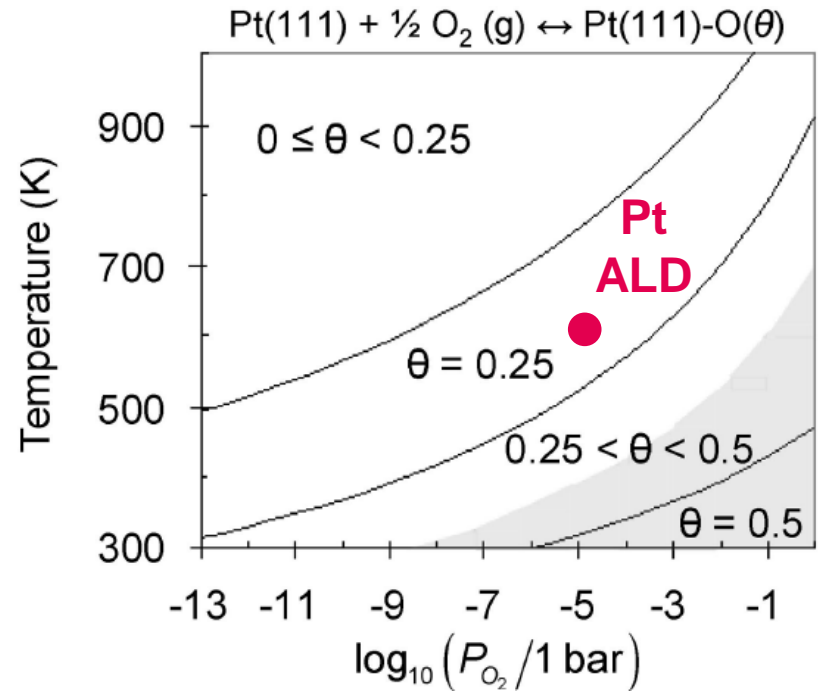
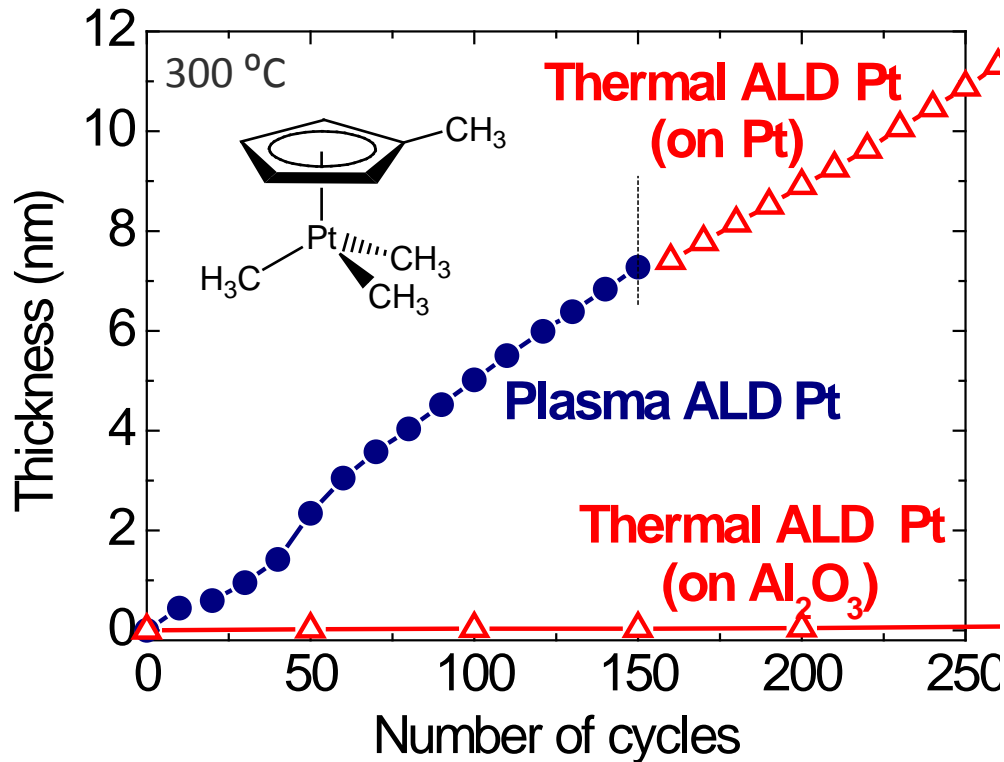
## 2. Building step: ALD



Patterning with:

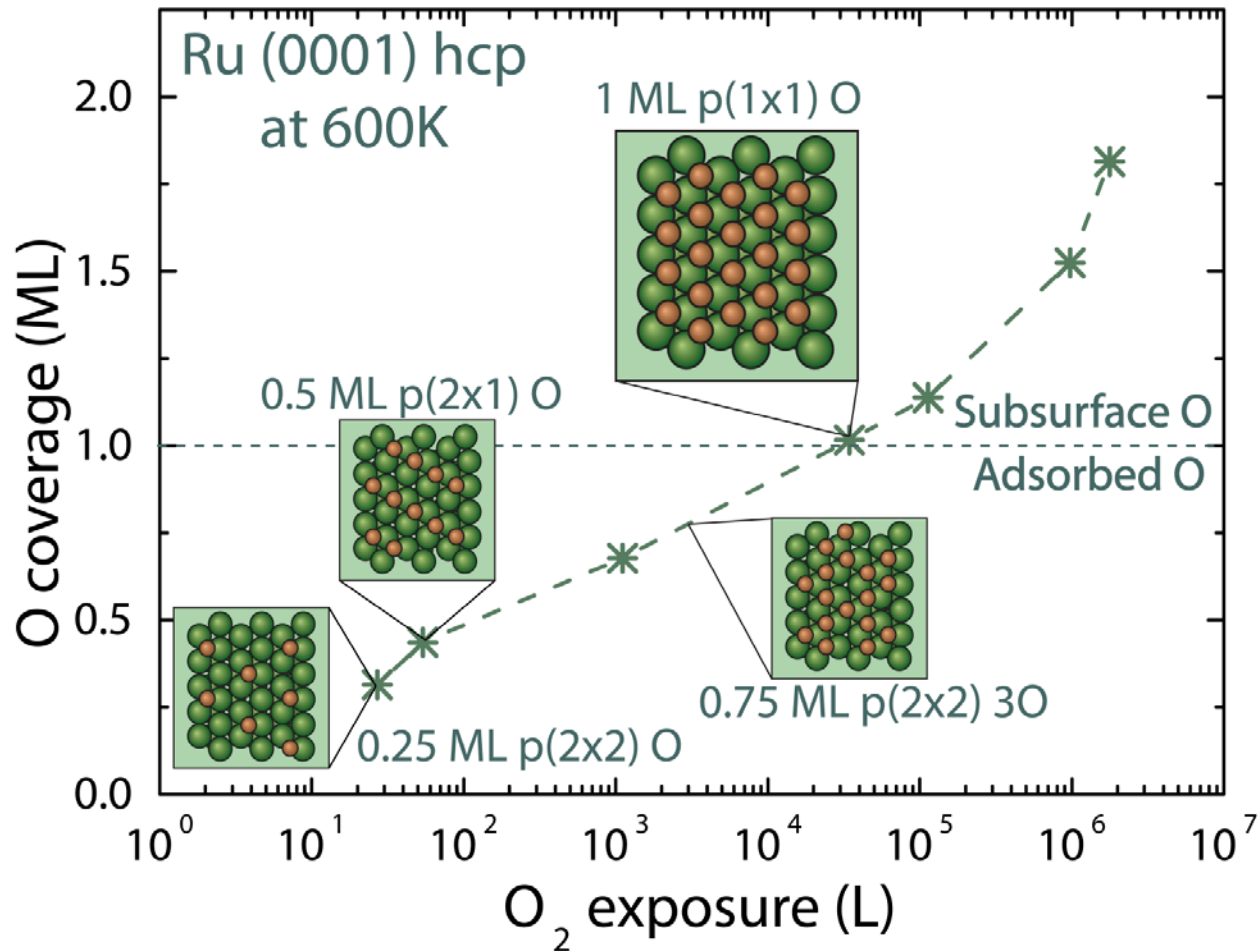
- **Nanoscale resolution** of electron beam induced deposition (EBID)
- High Pt **material quality** as obtained by ALD

# Underlying surface science: Pt dissociates O<sub>2</sub>



Pt nucleates **poorly** on oxides  
 Nucleation depends on **O<sub>2</sub> pressure** and **sample temperature**

# Underlying surface science: Ru dissociates O<sub>2</sub>

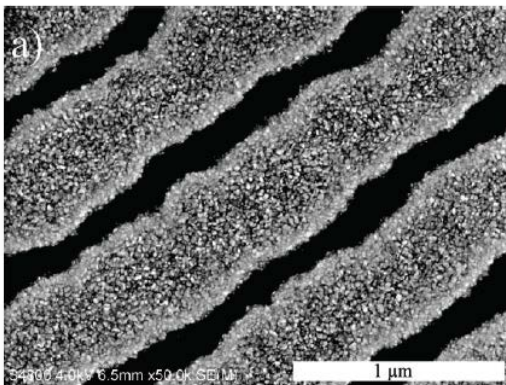
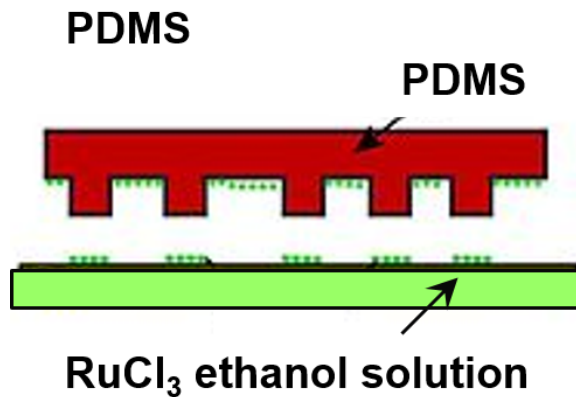




# AS-ALD by area-activation: $\mu$ -CP & ALD of Ru

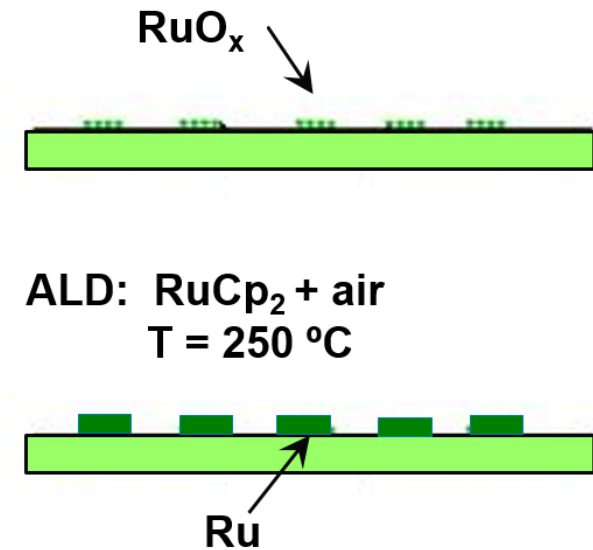
## 1. Patterning step:

Micro-contact printing



## 2. Building step:

Atomic layer deposition (ALD)



Ru temperature window = 275 – 400 °C

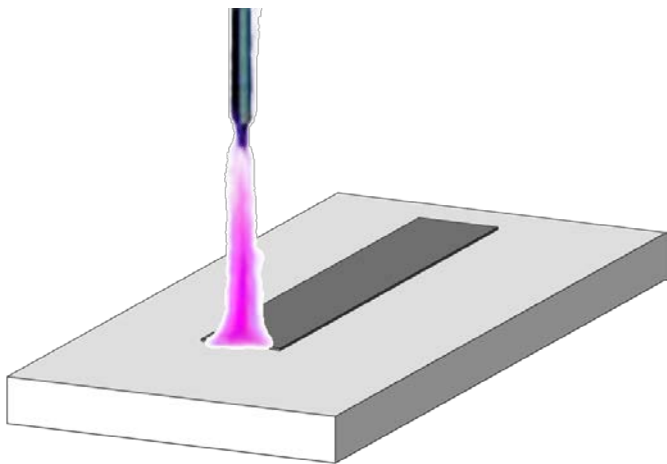
Ru does **not nucleate** just below temperature window: **250 °C**

# Outline

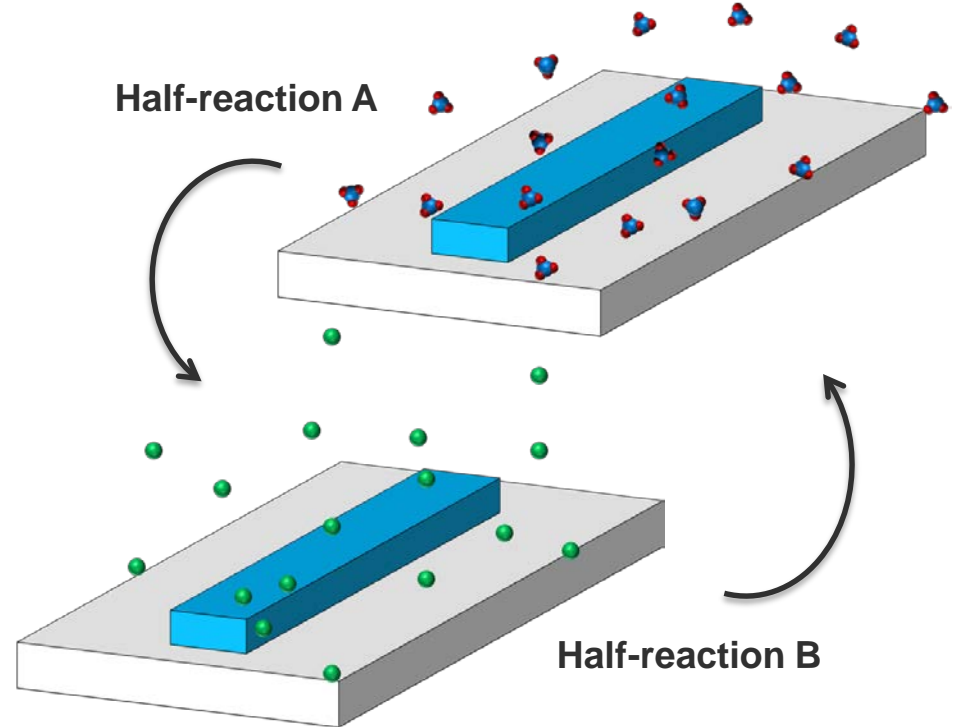
- Introduction & Area-selective deposition
- Area-selective ALD by **area-deactivation**
  - Example: **blocking with polyimide**
- Area-selective ALD by **area-activation**
  - Example: **EBID & ALD of Pt (direct-write ALD)**
  - Intermezzo:  $\mu$ -contact printing & ALD of Ru
  - Example:  **$\mu$ -plasma printing & ALD of  $\text{In}_2\text{O}_3$  (direct-write ALD)**
- Summary

# AS-ALD by area-activation: $\mu$ PP & ALD of $\text{In}_2\text{O}_3$

1. **Patterning step:**  
 $\mu$ -plasma printer (in air)



2. **Building step:**  
Atomic layer deposition (ALD)



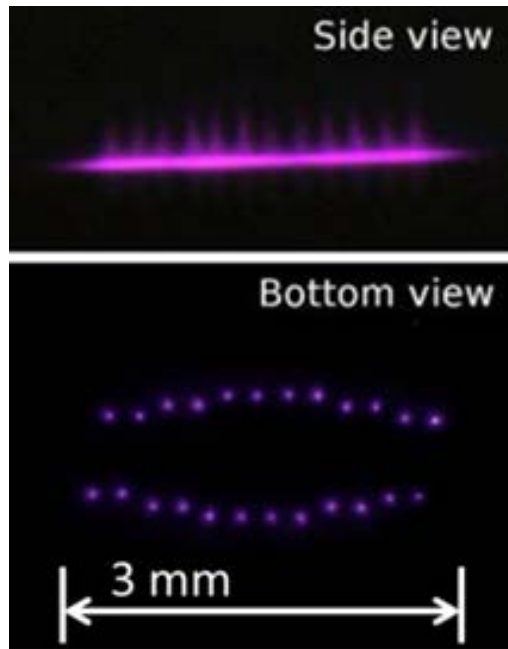
Two-step process:

- Patterning: **plasma treatment** of H-terminated silicon surface
- Building: **area-selective ALD** on treated area

# AS-ALD by area-activation: $\mu$ -PP & ALD of $\text{In}_2\text{O}_3$

## 1. Patterning step:

$\mu$ -plasma printer (in air)

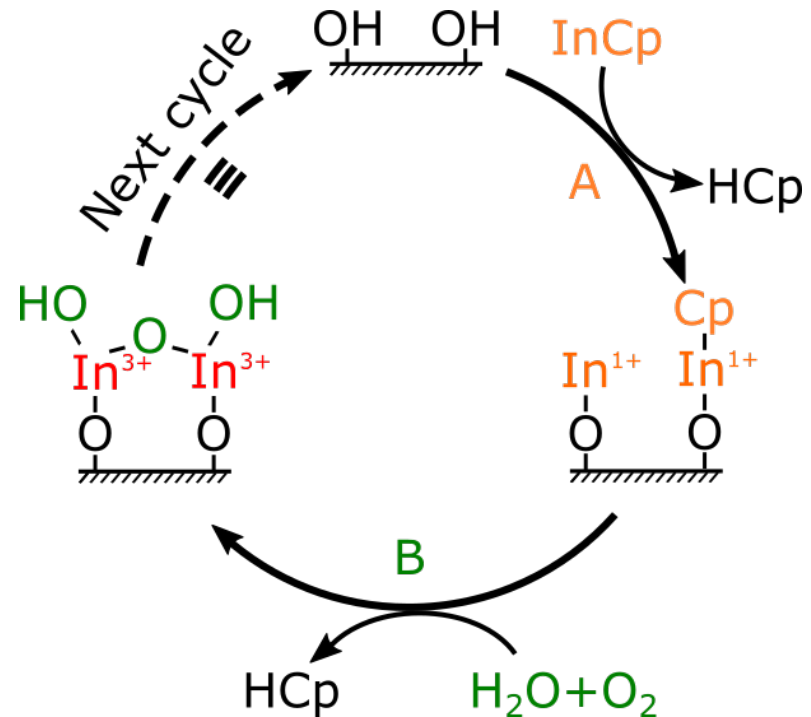


Patterning with:

- **Sub-mm resolution** of  $\mu$ -plasma printer
- High  $\text{In}_2\text{O}_3$  **material quality** as obtained by ALD

## 2. Building step:

Atomic layer deposition (ALD)



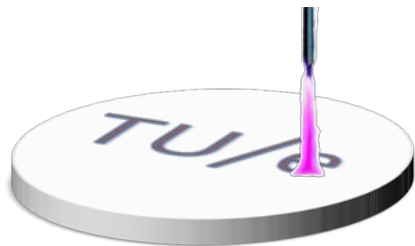
# Direct-write ALD of $\text{In}_2\text{O}_3$

1)



Blank H-terminated surface

2)



$\mu$ -plasma activation

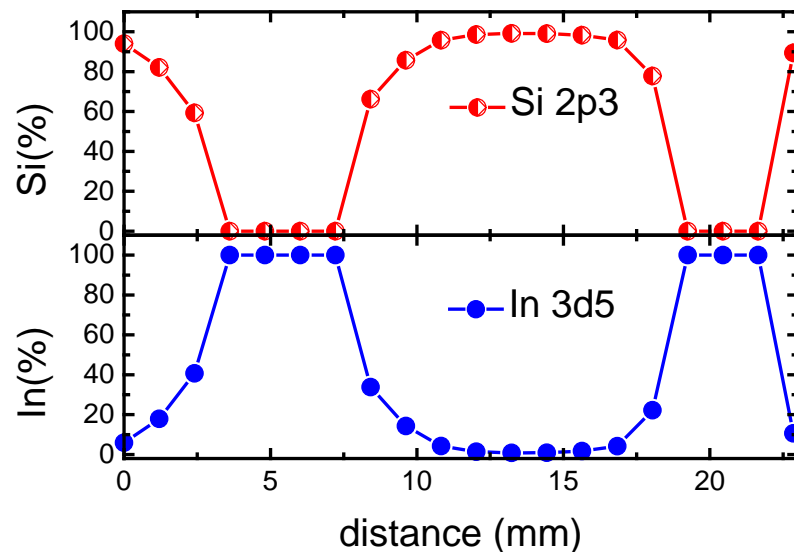
3)



ALD

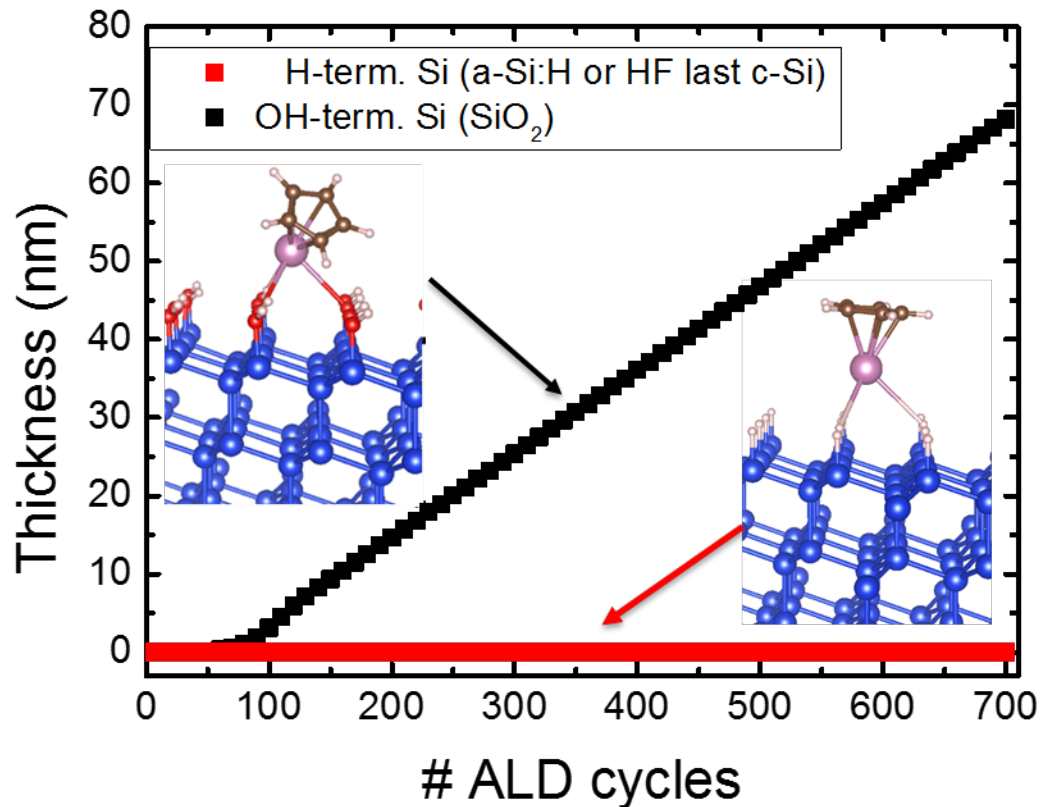


XPS linear scan





# Underlying surface science: InCp adsorption



Adsorption energies of InCp (in eV)

**Si(111)-H**

PBE (Avg.)	-0.021
PBE-D3 (Avg.)	-0.258

**Si(111)-OH**

PBE (Avg.)	-0.048
PBE-D3 (Avg.)	-0.385

Calculations in progress:  
Adsorption on Si(111)-OH seems  
thermodynamically more  
favorable

ALD In<sub>2</sub>O<sub>3</sub> using InCp as precursor exhibits a **large nucleation delay** on **H-terminated surfaces**

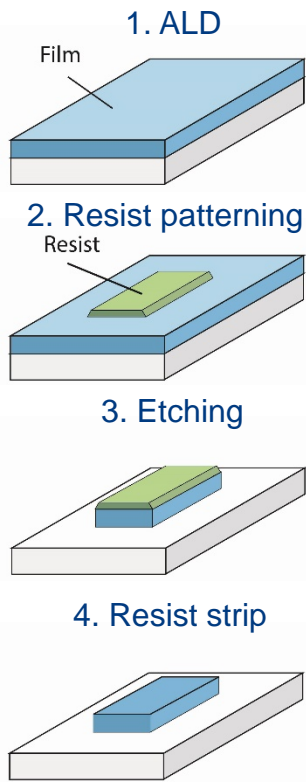
# Summary

- Area-selective deposition will provide **processing solutions** for upcoming technology nodes
- There is especially interest in area-selective ALD as ALD is **starting surface dependent**
- Preferably also **nanopatterning** by area-selective ALD
- We can distinguish two approaches:
  - Area-selective ALD by **area-deactivation**
  - Area-selective ALD by **area-activation**
- Area-selective ALD by area-activation is the ultimate dream as the **surface on which no film needs to be deposited remains untouched**
- Presented two approaches that can be categorized as **direct-write ALD**
- To be continued... !

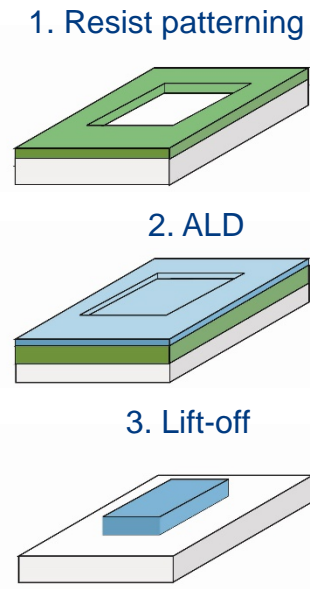
# Summary: ALD-enabled patterning

## Conventional:

### Etching

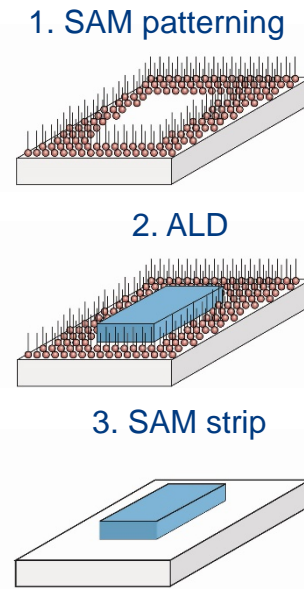


### Lift-off

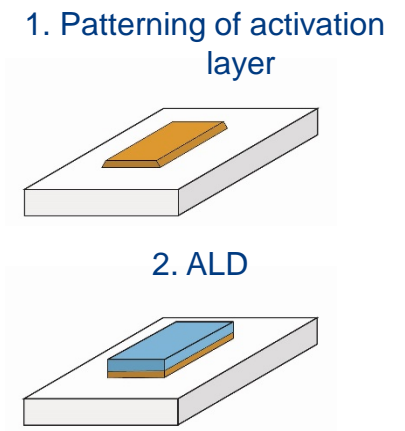


## ALD-enabled:

### Area-selective ALD by area-deactivation



### Area-selective ALD by area-activation



# Review paper



Nanoscale

REVIEW



Cite this: *Nanoscale*, 2014, 6, 10941

## The use of atomic layer deposition in advanced nanopatterning

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Atomic layer deposition (ALD) is a method that allows for the deposition of thin films with atomic level control of the thickness and an excellent conformality on 3-dimensional surfaces. In recent years, ALD has been implemented in many applications in microelectronics, for which often a patterned film instead of full area coverage is required. This article reviews several approaches for the patterning of ALD-grown films. In addition to conventional methods relying on etching, there has been much interest in nanopatterning by area-selective ALD. Area-selective approaches can eliminate compatibility issues associated with the use of etchants, lift-off chemicals, or resist films. Moreover, the use of ALD as an enabling technology in advanced nanopatterning methods such as spacer defined double patterning or block copolymer lithography is discussed, as well as the application of selective ALD in self-aligned fabrication schemes.

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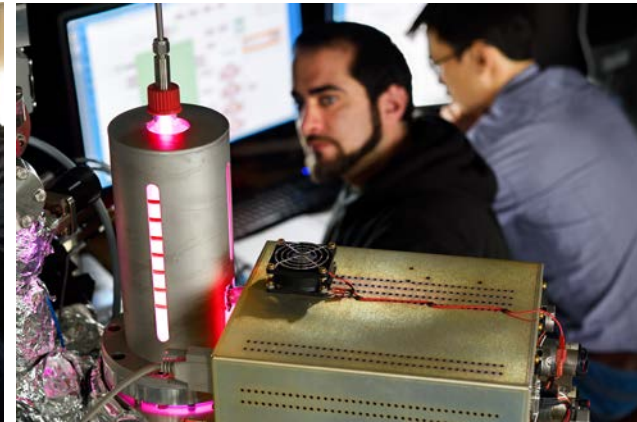
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